



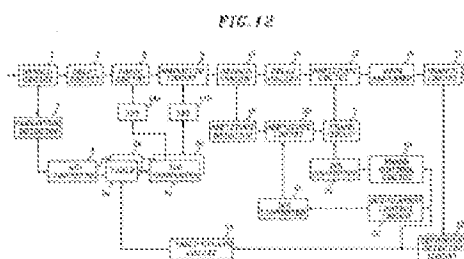


Distortion compensation circuit**Publication number:** CN1409482**Publication date:** 2003-04-09**Inventor:** TOSHIO TAKATA (JP); YOICHI OKUBO (JP); MASAKI SUTO (JP)**Applicant:** HITACHI INTERNAT ELECTRIC CO L (JP)**Classification:****- International:** **H03F1/32**; H03F1/32; (IPC1-7): H03F1/00; H03F1/32**- European:** H03F1/32P12**Application number:** CN20021043556 20020927**Priority number(s):** JP20010296812 20010927; JP20010297632 20010927**Also published as:** EP1300940 (A2) US6809588 (B2) US2003085762 (A1) EP1300940 (A3)[Report a data error here](#)

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A distortion compensation circuit reduces an unbalance between higher and lower 3rd order distortions, the distortions being generated by an amplifier which amplifies an input signal having at least two frequency components. Amplitude modulation means, which includes an amplitude detector, an amplitude circuit, a phase circuit and another amplitude circuit, generates sideband signals corresponding to an instantaneous phase difference between the higher and lower 3rd order distortions, such that distortions generated by the amplifier can be cancelled. Further, sideband signal generating means, which includes an amplitude circuit, a delay circuit, an AM modulator, another amplitude circuit, another delay circuit and a PM modulator, performs AM and PM on the input signal to thereby generate sideband signals, such that distortions generated by the amplifier can be cancelled.

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[71] 申请人 株式会社日立国际电气

地址 日本东京都

[72] 发明人 高田寿雄 大久保阳一 须藤雅树
洞口正人 本江直树

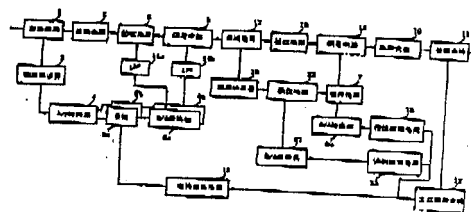
[74] 专利代理机构 永新专利商标代理有限公司
代理人 夏青

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[54] 发明名称 失真补偿电路

[57] 摘要

一种失真补偿电路，可以降低由放大器所产生的高三阶失真和低三阶失真之间的不平衡，该放大器放大具有至少两个频率分量的输入信号。幅度调制单元包括一个幅度检波器、一个幅度电路、一个相位电路和另一个幅度电路，它产生对应于高三阶和低三阶失真之间瞬时相差的边带信号，从而能够消除该放大器所产生的失真。而且，边带信号产生单元包括一个幅度电路、一个延迟电路、一个 AM 调制器、另一个幅度电路、另一个延迟电路和一个 PM 调制器，它对输入信号执行 AM 和 PM，由此产生边带信号，从而能够消除该放大器所产生的失真。



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